

PATENT NUMBER

U.S. UTILITY Patent Application

O.J.P.E.

PATENT DATE

SCANNED

4 km² a.

A. VVO

APPLICATION NO.
09/918511

CONT/PRIOR
F

CLASS
438

SUBCLASS	2
----------	---

ART UNIT
2812

EXAMINER

APPLICANTS

Yuan-Tung Dai
Chi-Shen Lee
Jiun-Jye Chang

TITLE

Manufacturing method of thin film transistor panel

ISSUING CLASSIFICATION

[illegible]

☐ Continued on Issue Slip Inside File Jacket

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED			
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.	
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)			NOTICE OF ALLOWANCE MAILED		
	<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ (Primary Examiner) (Date)			ISSUE FEE	
					Amount Due	
<input type="checkbox"/> The terminal ____months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)			ISSUE BATCH NUMBER		
WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368. Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.						

Form **PTO-436A**
(Rev. 6/99)

FILED WITH: ☐ DISK (CRF) ☐ FICHE ☐ CD-ROM

(Attached in pocket on right inside flap)

(FACE)